

Compal Confidential

NYU00 LA-5301P R02 Schematics Document

Menlow-Silverthorne with Poulsbo

Monday, April 06, 2009

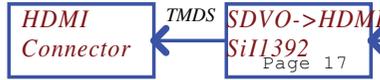
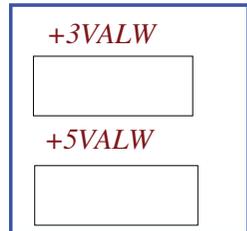
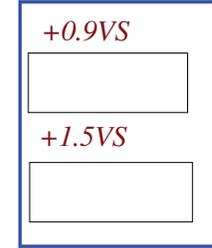
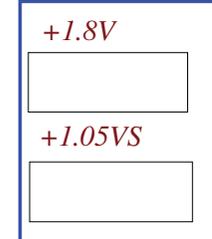
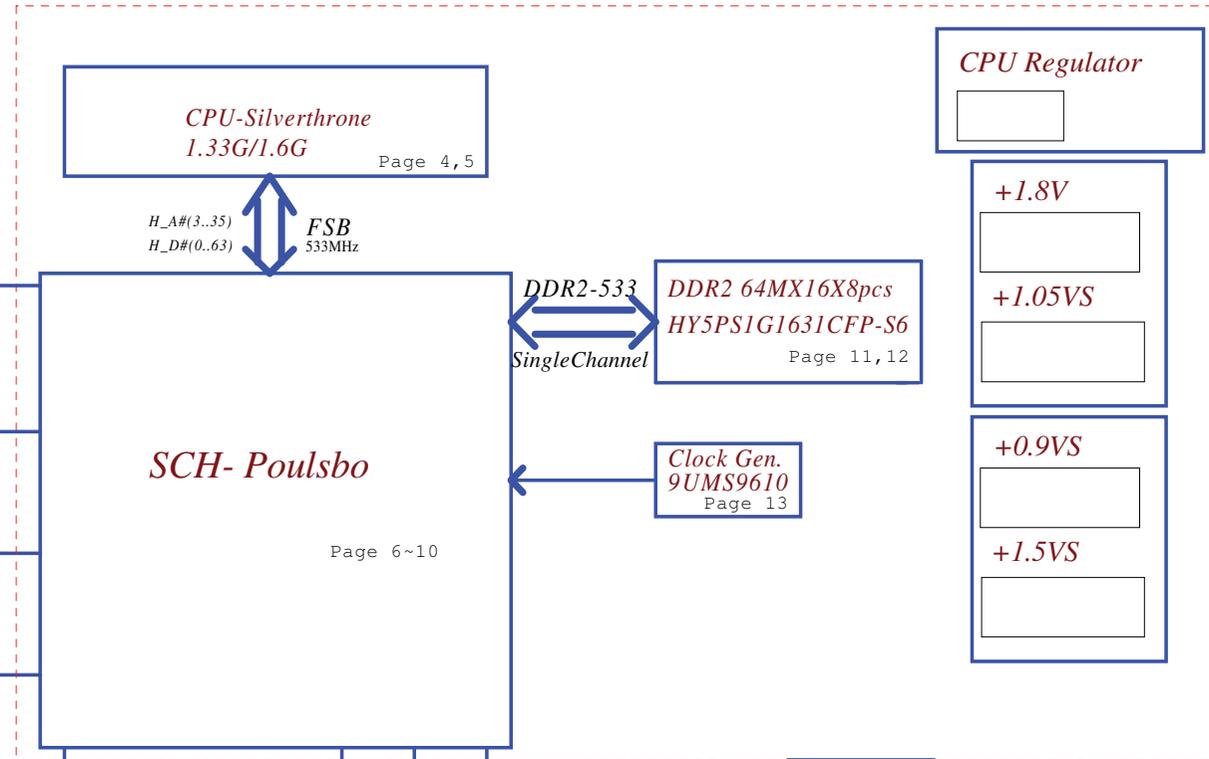
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Model Name :

LA-5301P

CPU-Board



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Title			
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Block Diagram			
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Voltage Rails

○ MEANS ON X MEANS OFF

power plane / State	+VBAT	+5VALW +3VALW	+1.8V +0.9V	+5VS +3VS +1.8VS +1.5VS +CPU_CORE +VCCP	CLOCK
S0	○	○	○	○	○
S3	○	○	○	X	X
S5 S4/AC	○	○	X	X	X
S5 S4/ Battery only	○	X	X	X	X
S5 S4/AC & Battery don't exist	X	X	X	X	X

○ MEANS ON
X MEANS OFF

S3 : STR
S4 : STD
S5 : SOFT OFF

BOM Structure

MARK	FUNCTION
@	NC FOR ALL

Address

USB PORT LIST

PORT	DEVICE
0	HOST LEFT1
1	HOST LEFT2
2	HOST RIGHT1
3	3G
4	BT
5	CAM
6	(RESERVE)
7	(RESERVE)

EC SM Bus1 address

Device	Address
GSENSOR MMA7455	0001_1101

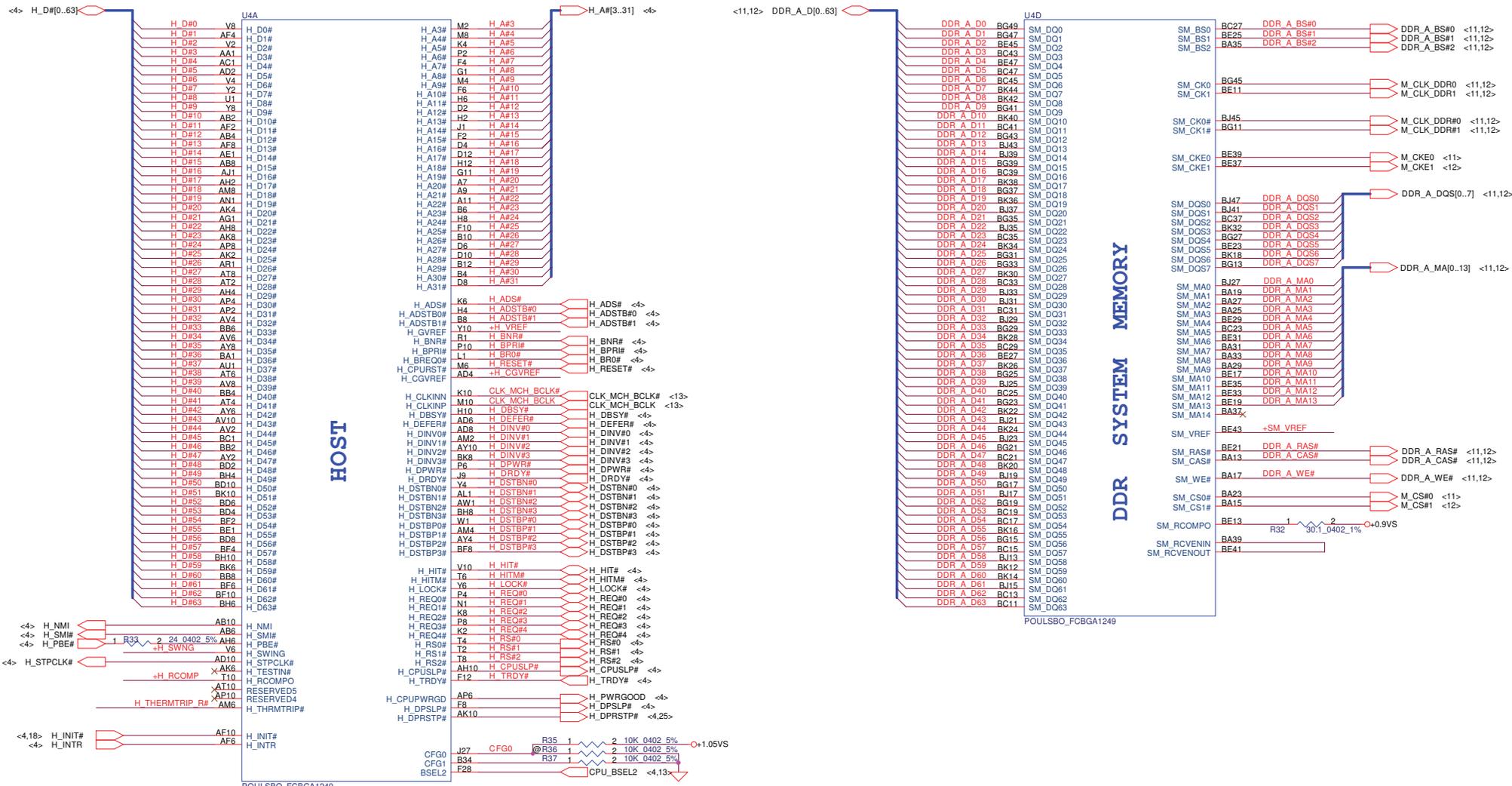
EC SM Bus2 address

Device	Address
THERMAL SENSOR 1 EMC1402-1-ACZL-TR	100_1100
THERMAL SENSOR 2 EMC1402-2-ACZL-TR	100_1101
LIGHT SENSOR TSL2561FN	ADDRSEL GND 0101001

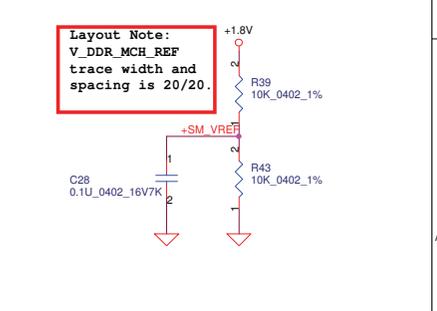
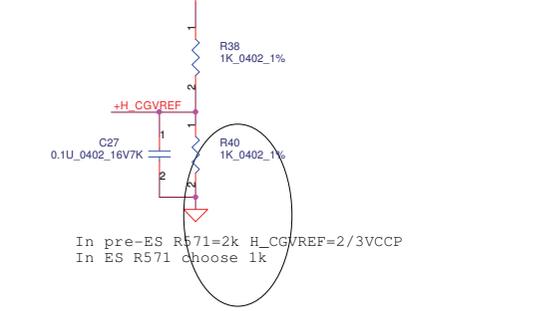
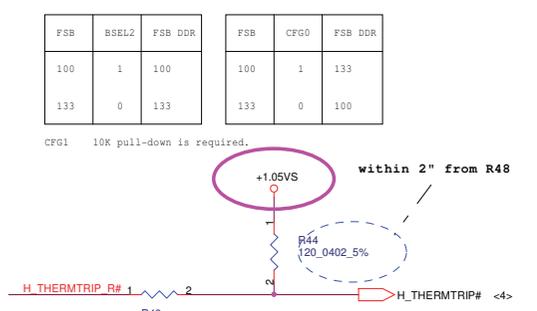
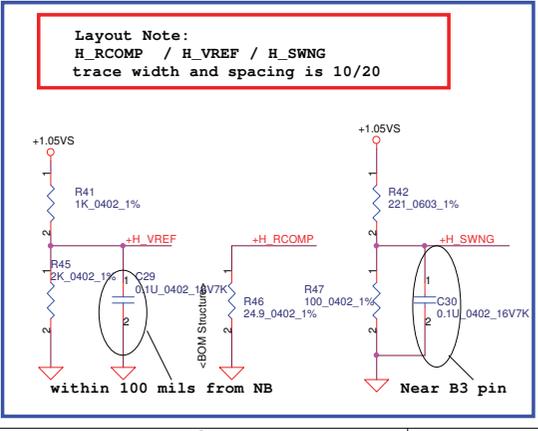
Poulsbo SM Bus address

Device	Address
Clock Generator (ICS90UMS9610BKLF-T)	1101 001Xb

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layout note:
Route H_SCOMP and H_SCOMP# with trace width, spacing and impedance (55 ohm) same as FSB data traces



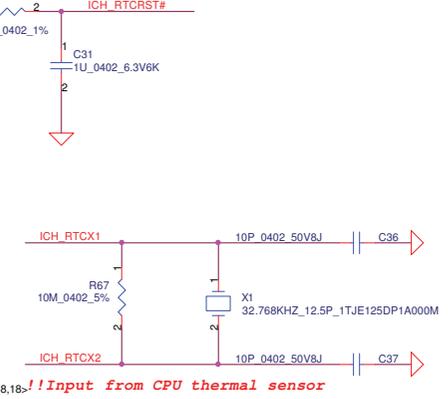
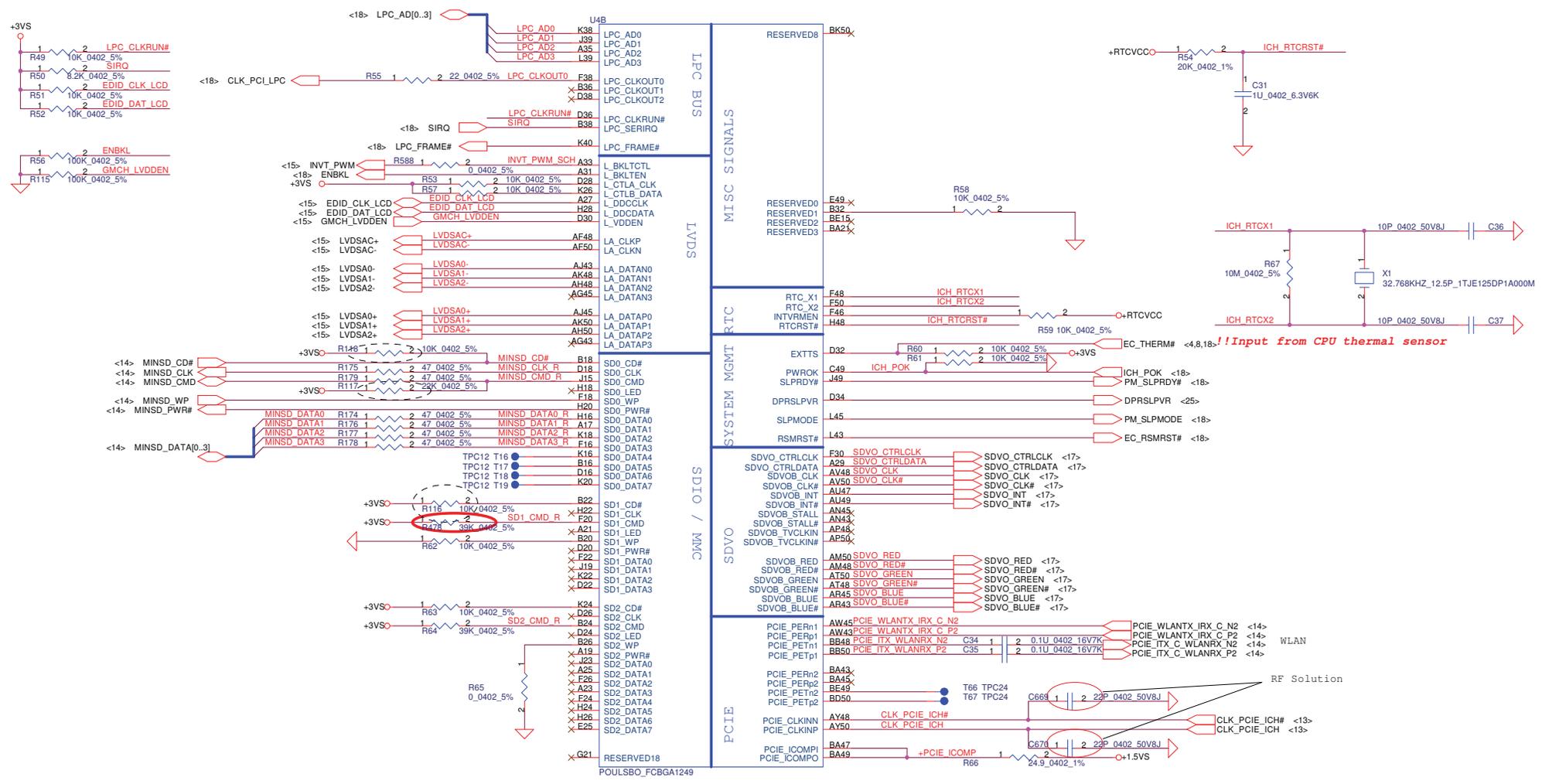
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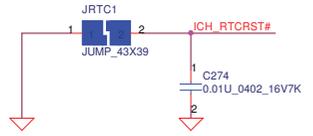
Poulsbo(15)-HOST/DDR

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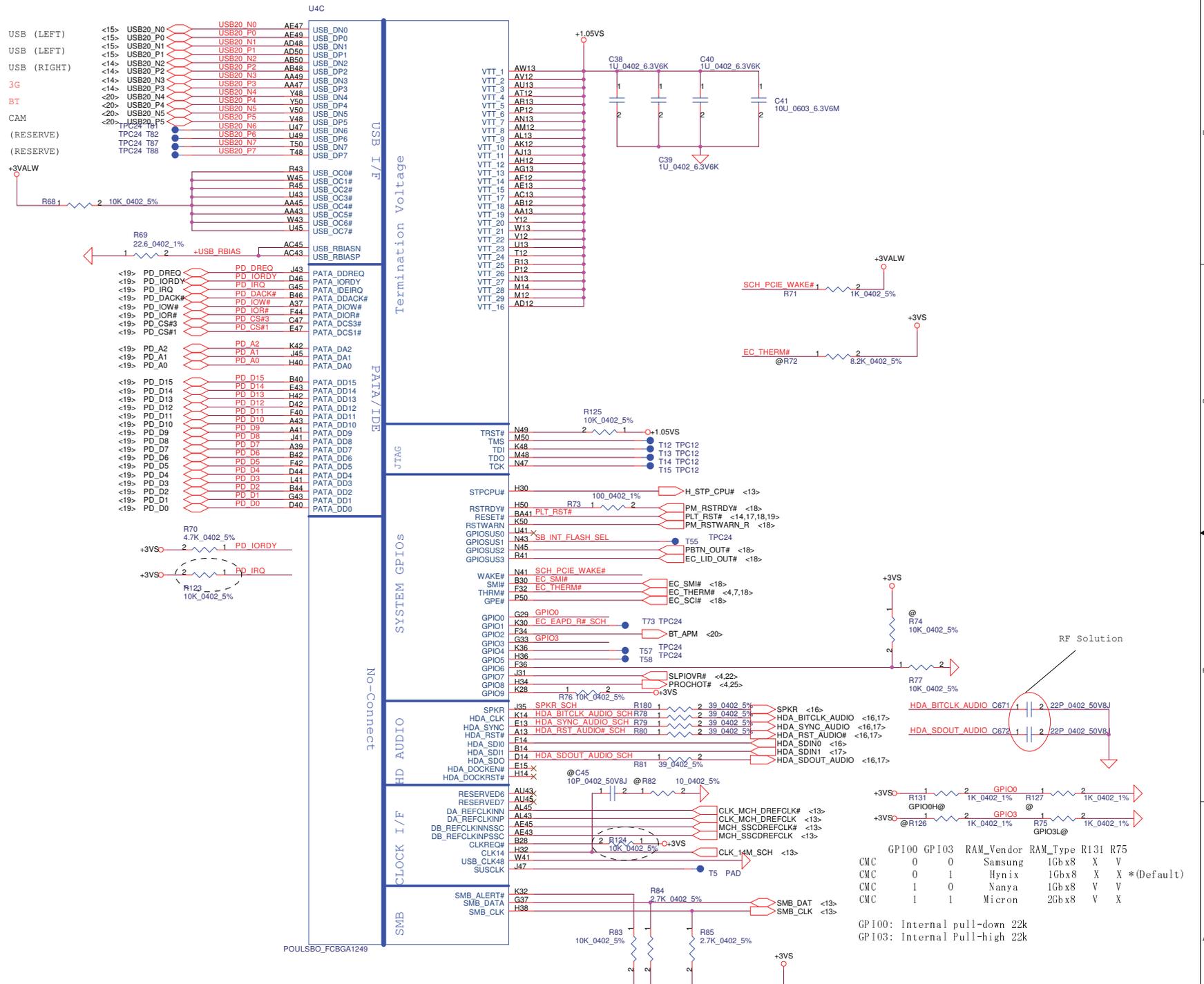
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**RTC Reset SW
(Place under door)**



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				Poulsbot(2/5)-LVDS/SDVO/SDIO/PCIE	
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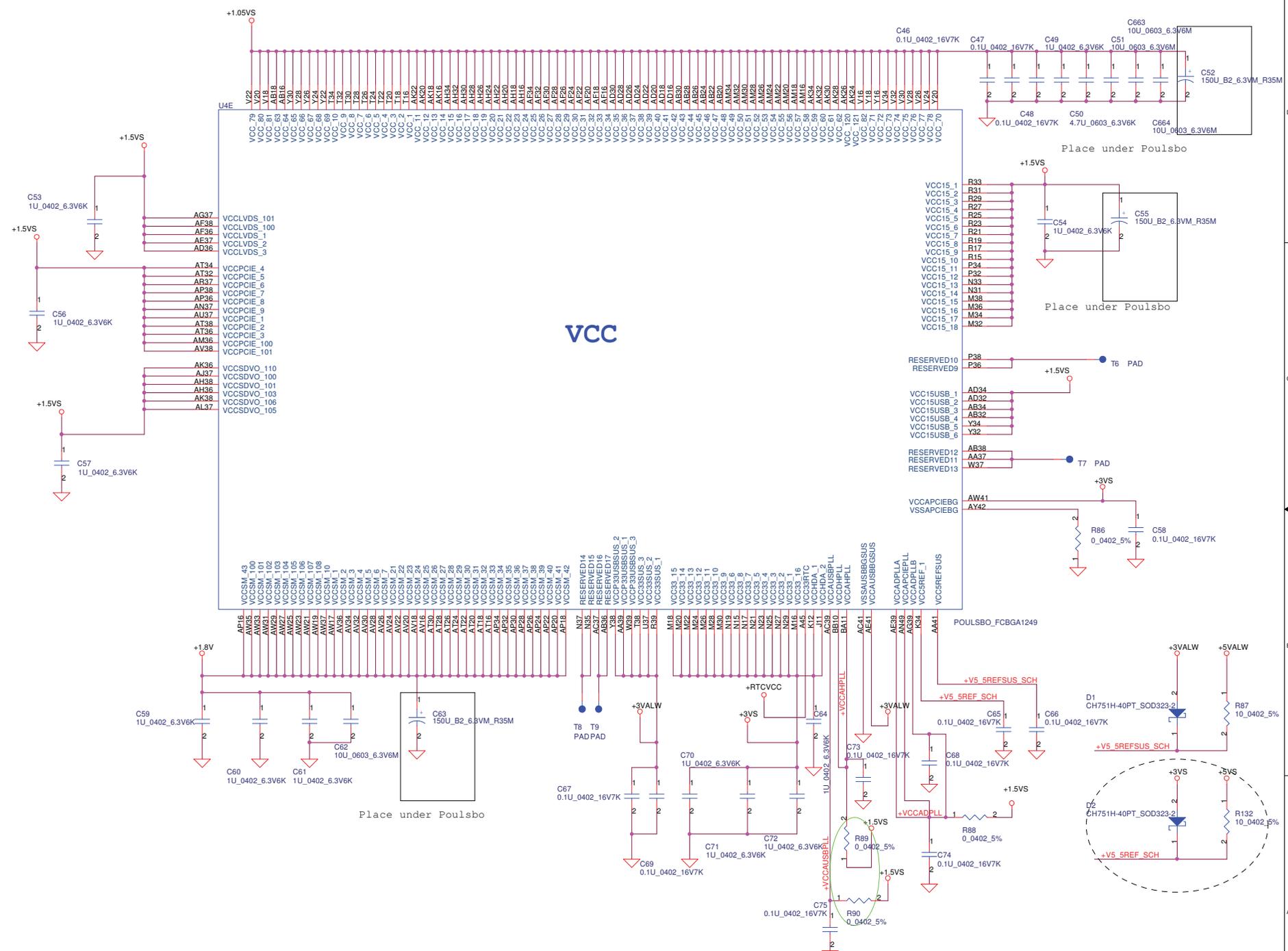


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Title			
Compal Electronics, Inc.			
Poulsbot(3/5)-HDA/PATA/USB			
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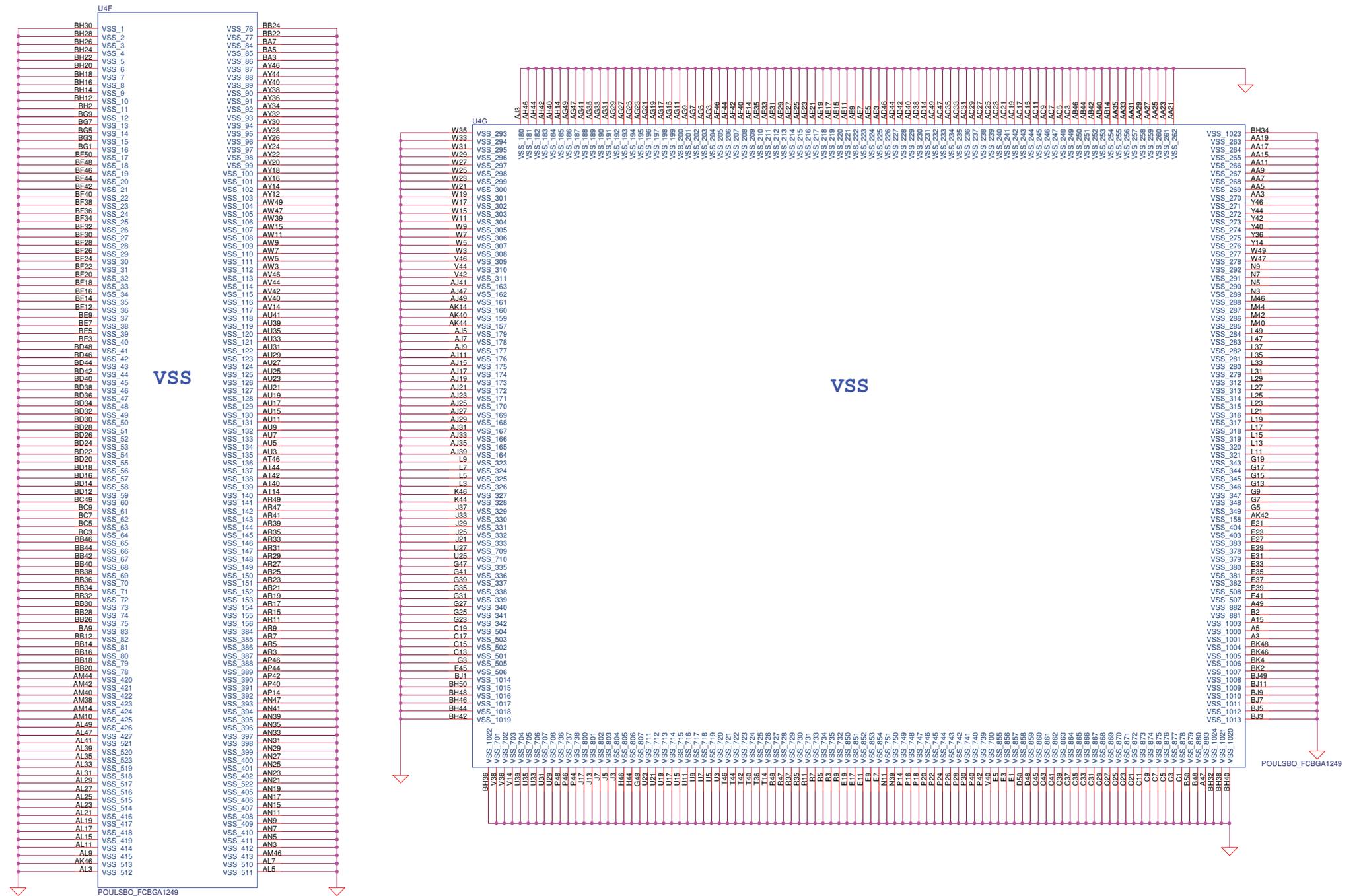
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0	0	0	Samsung	1Gb x8	X	V
0	1	1	Hynix	1Gb x8	X	X *(Default)
1	0	0	Nanya	1Gb x8	V	V
1	1	1	Micron	2Gb x8	V	X

GP100: Internal pull-down 22k
GP103: Internal Pull-high 22k

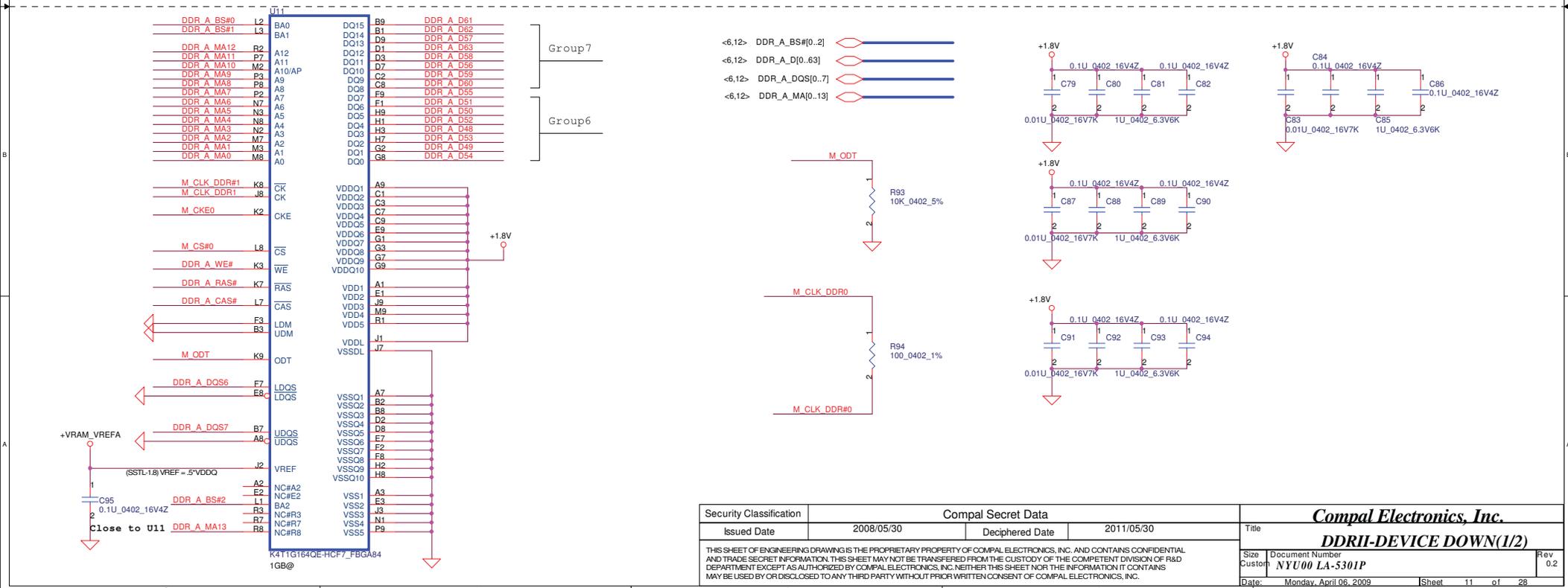
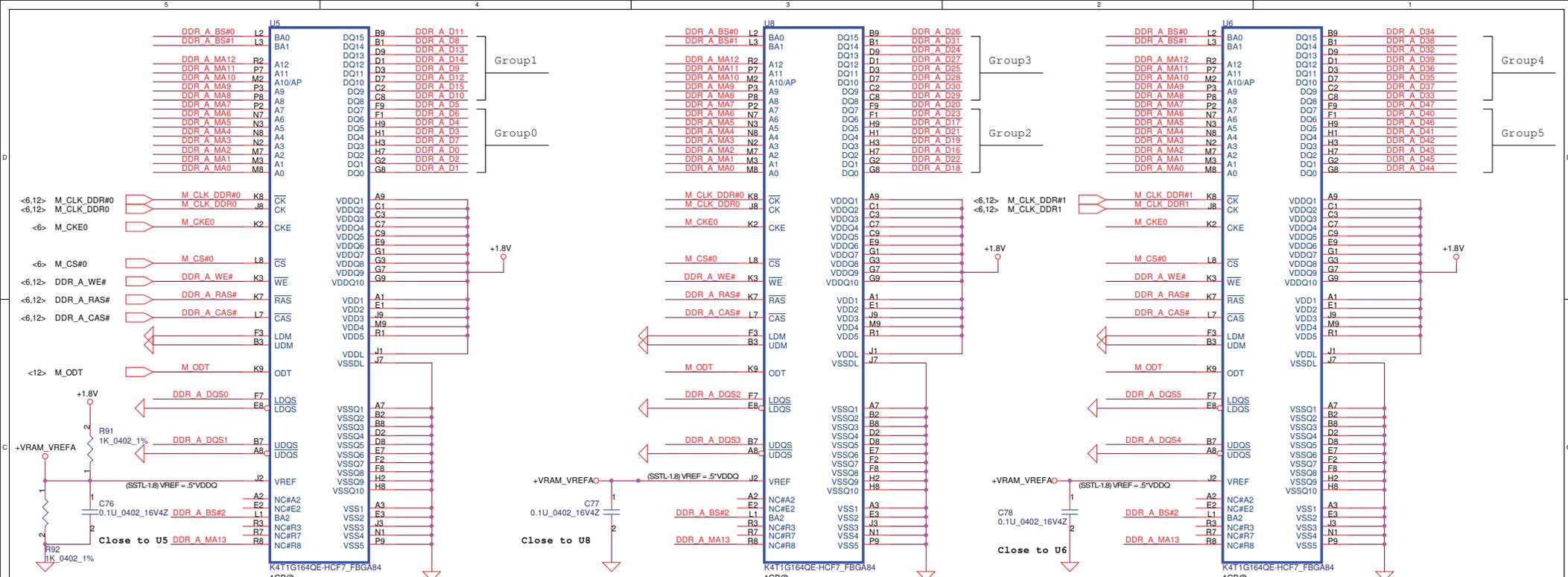


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Poulso(4/5)-PWR			
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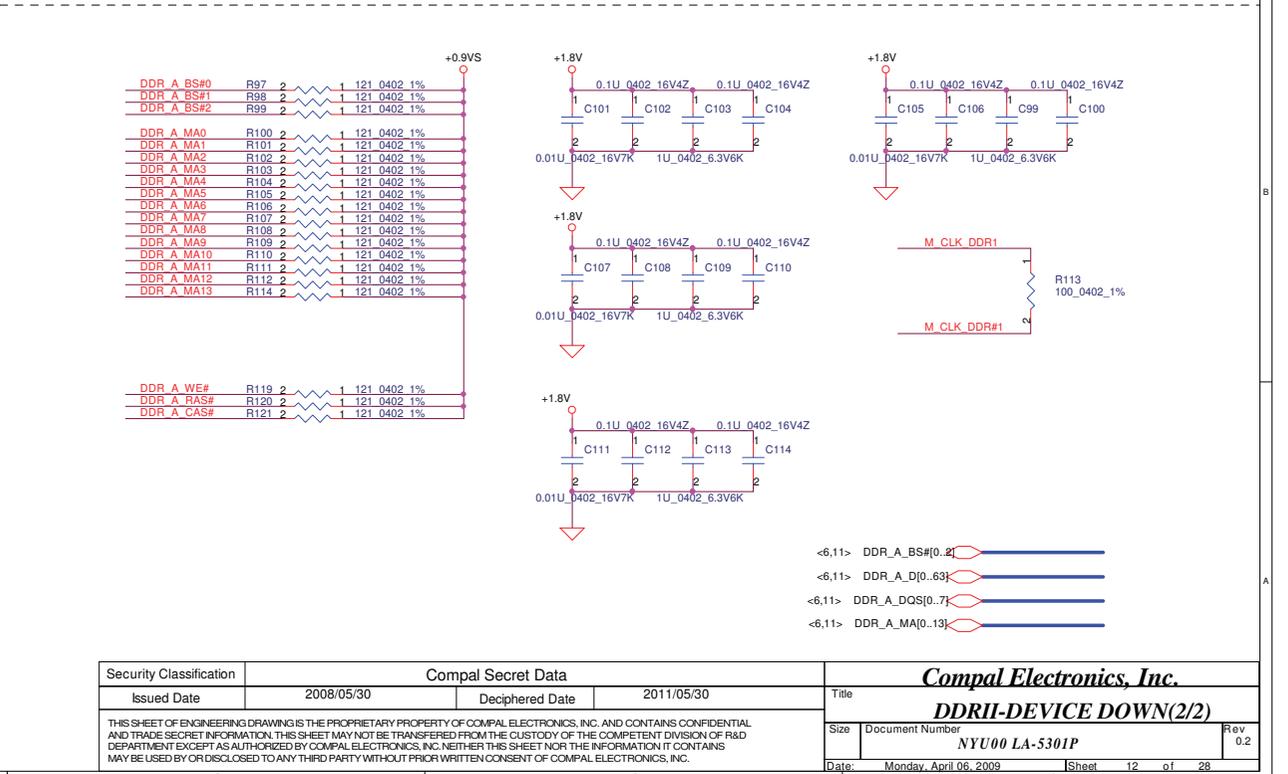
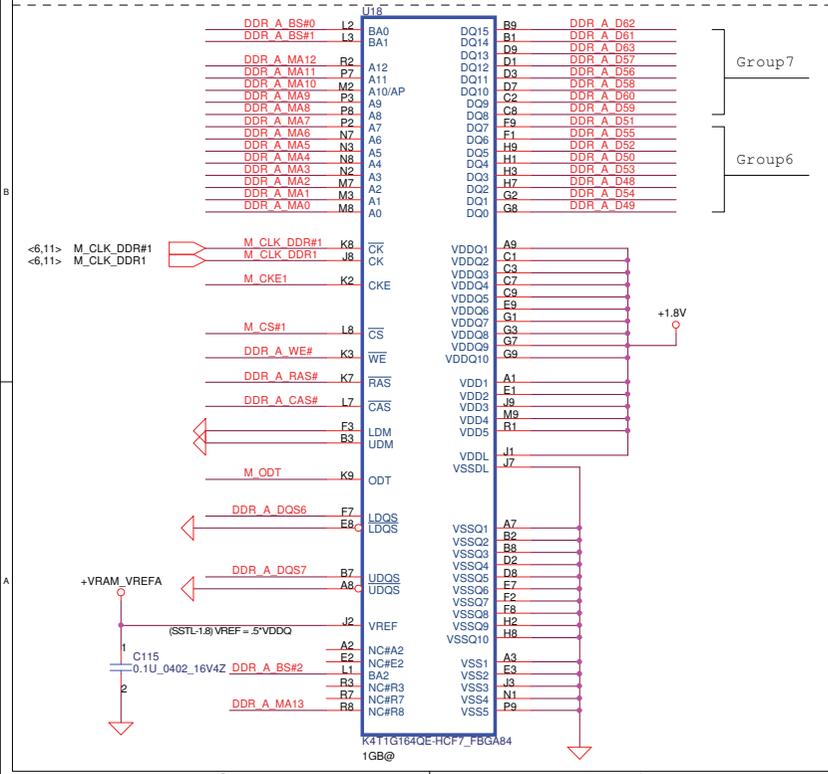
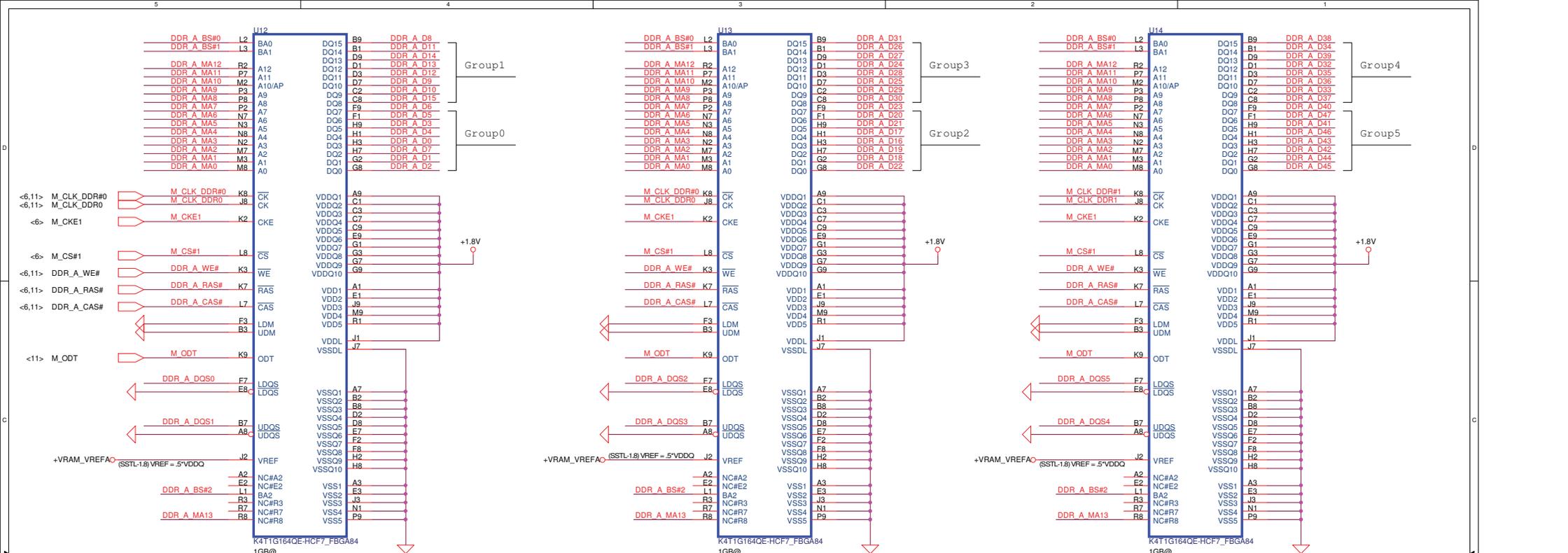
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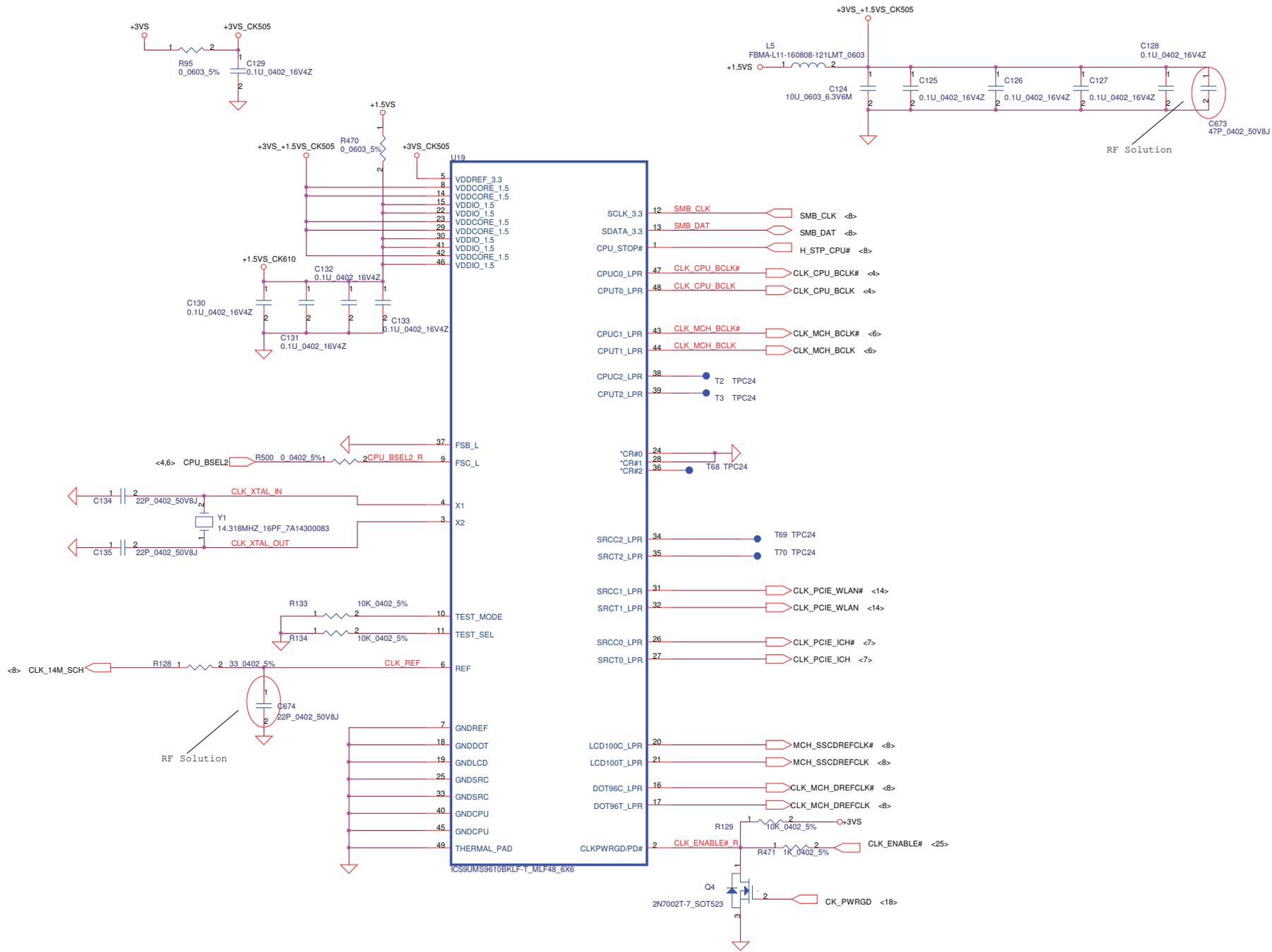
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DDRII-DEVICE DOWN(1/2)			
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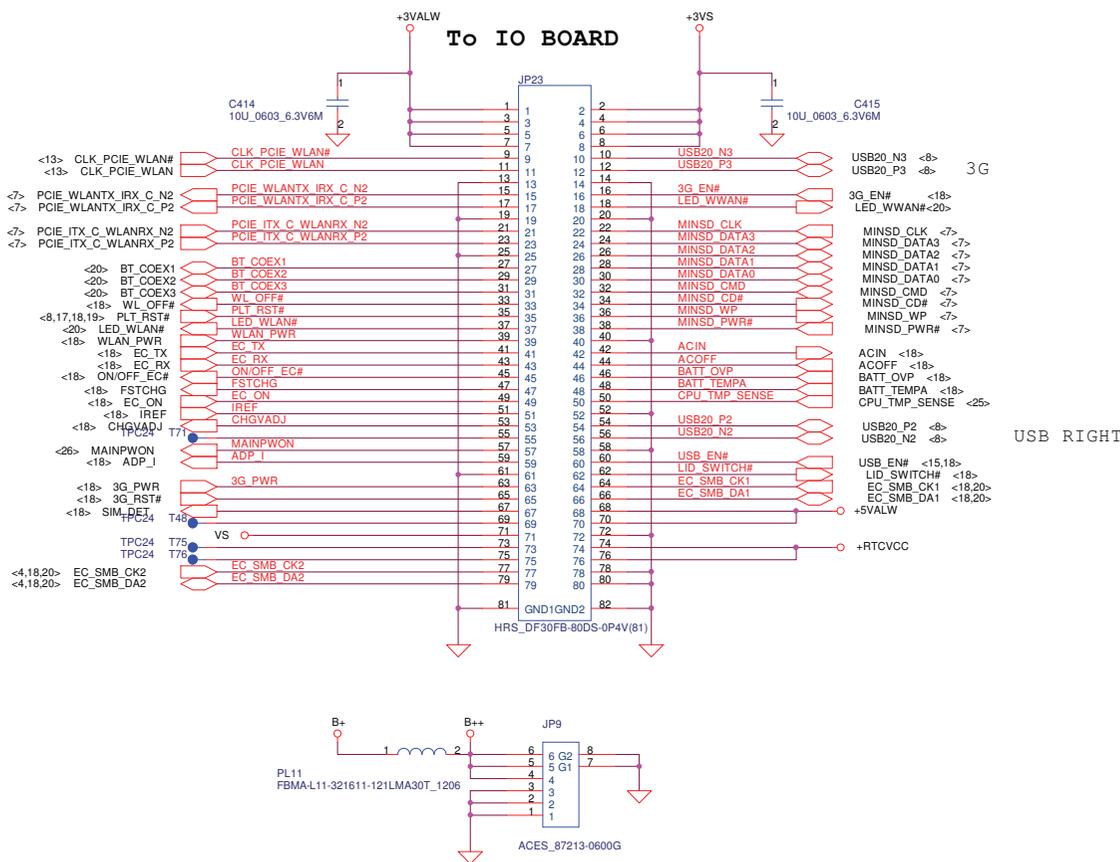
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				DDR II-DEVICE DOWN(2/2)	
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				Clock Generator CK610		
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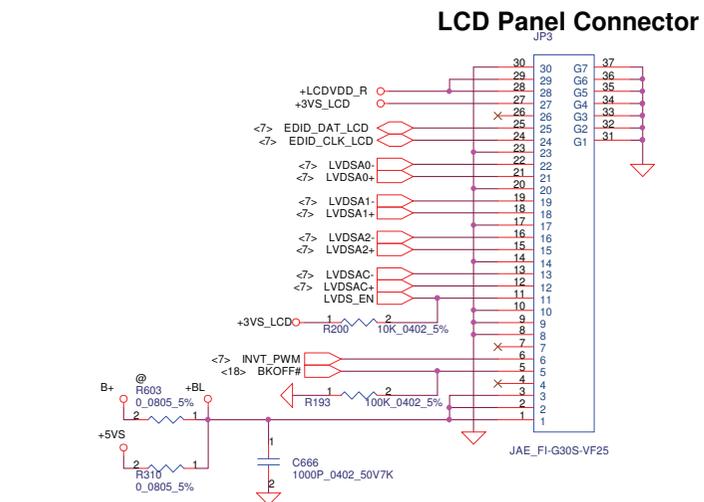
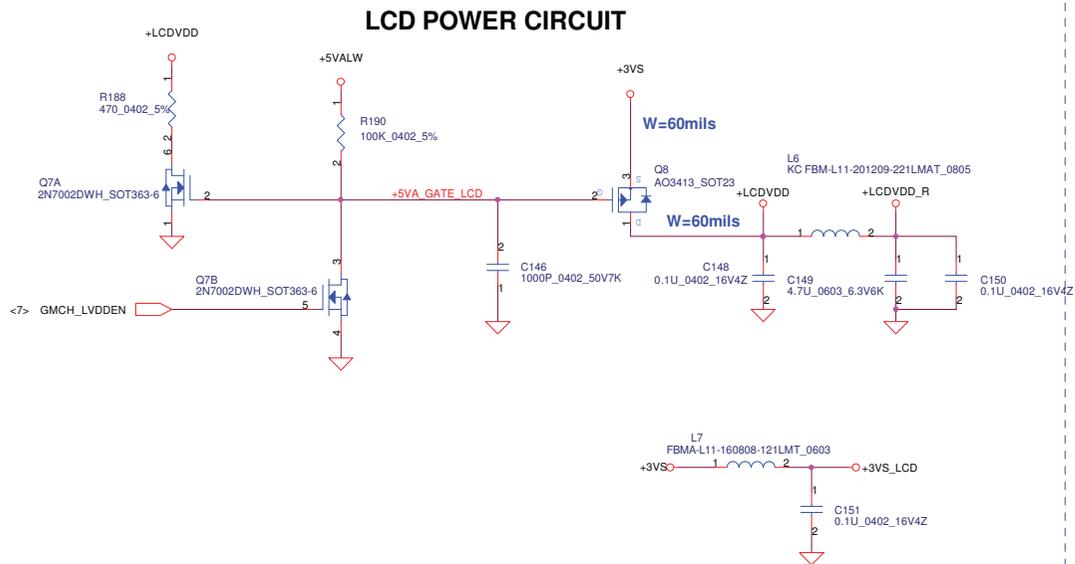


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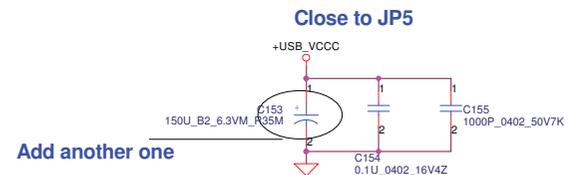
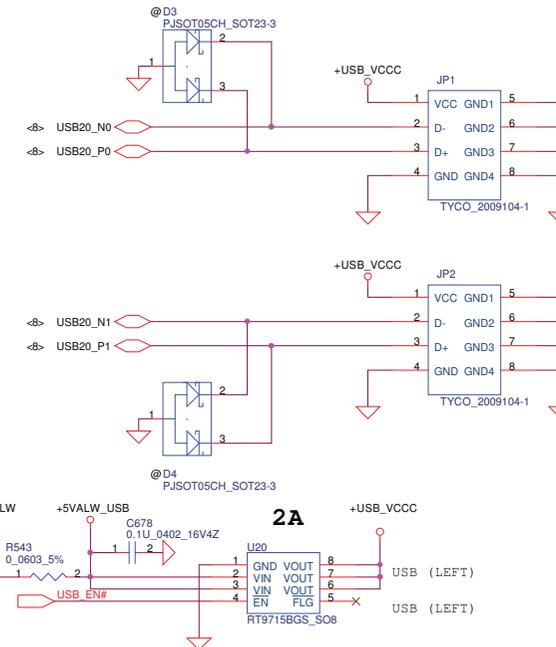
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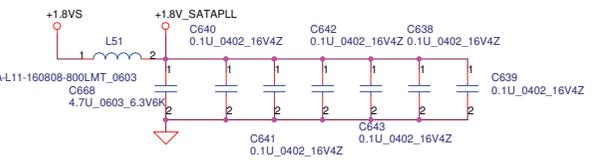
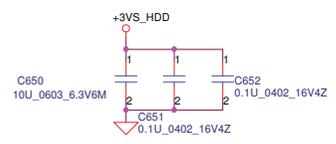
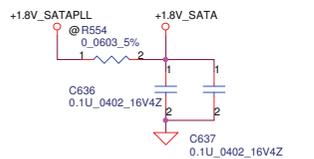
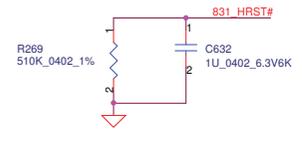
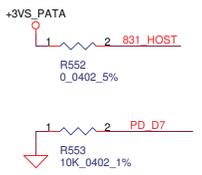
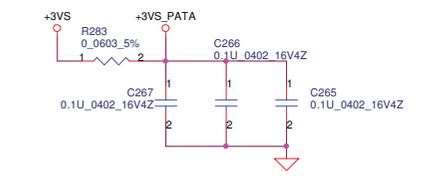
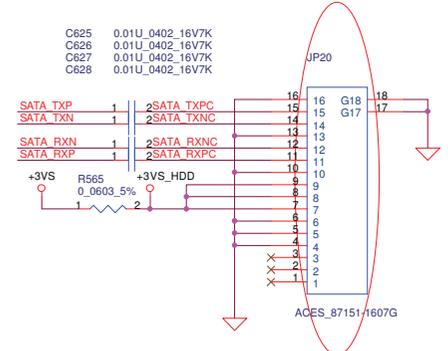
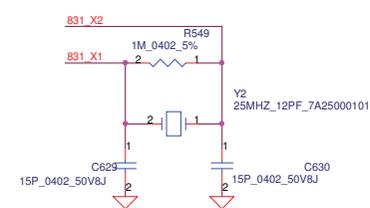
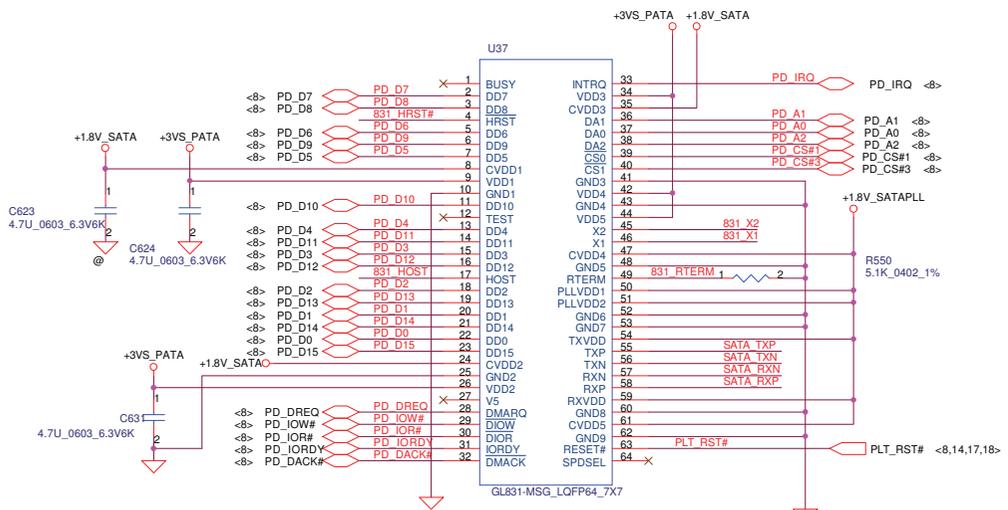
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USB/B Connector (Left)



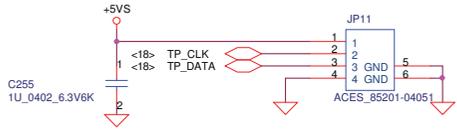
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				LCD/USB	
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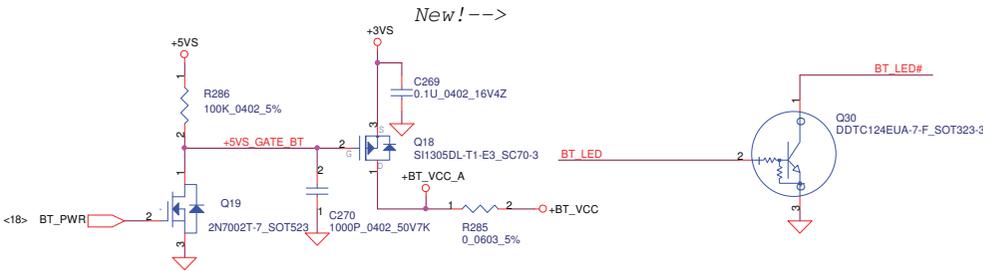
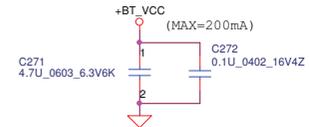
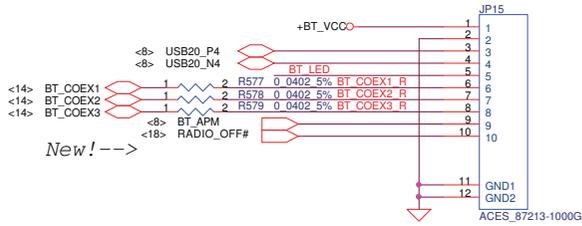
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Title PATA TO SATA			
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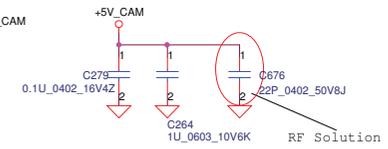
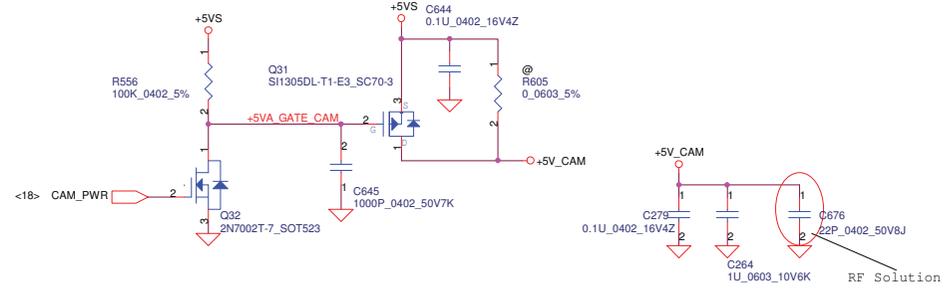
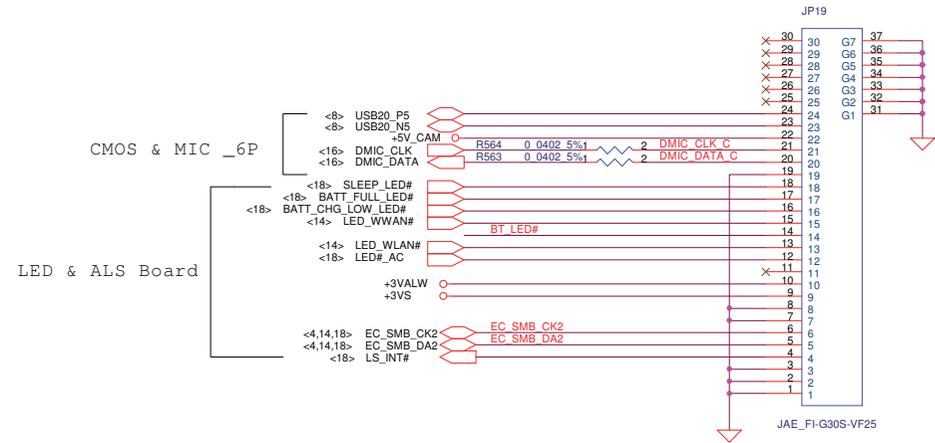
Touch/B Connector



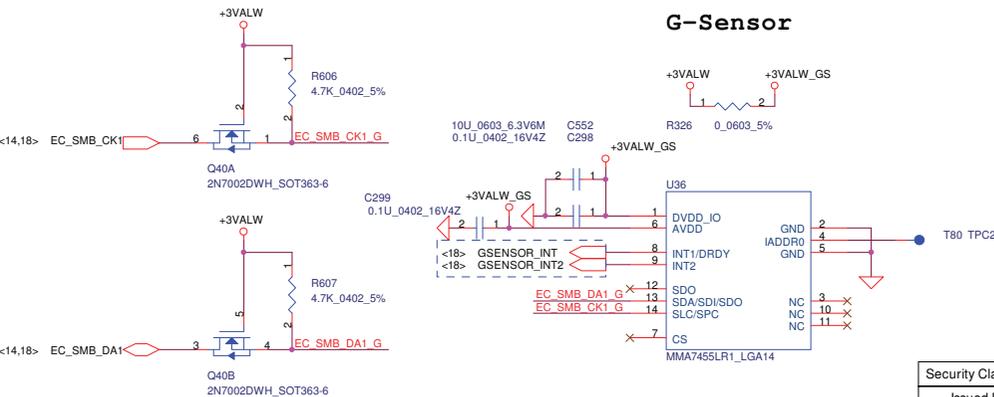
BlueTooth Connector



GPS + CMOS Camera/MIC + LED Board Connector

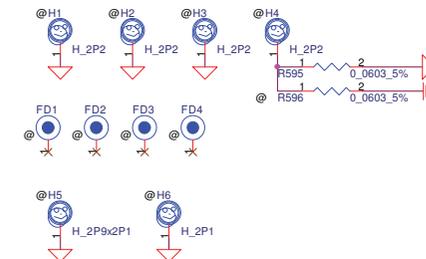
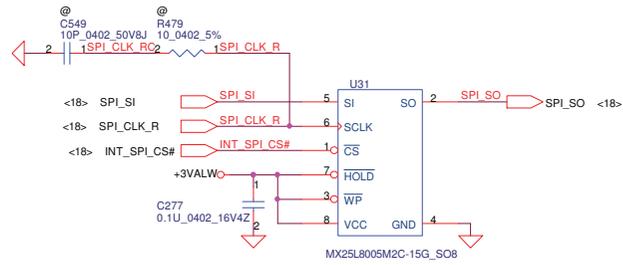


G-Sensor

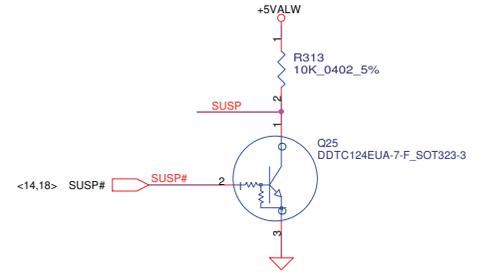
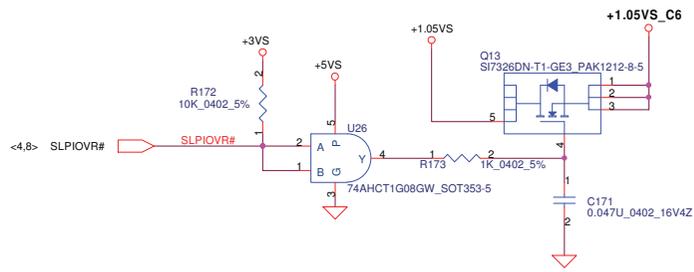


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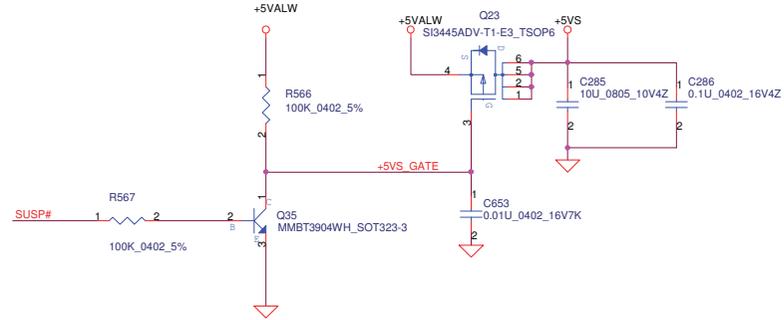
System SPI Flash ROM (8Mb)



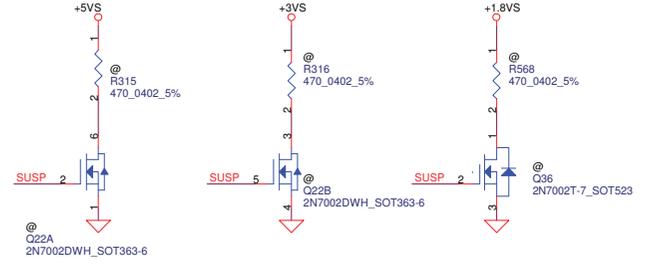
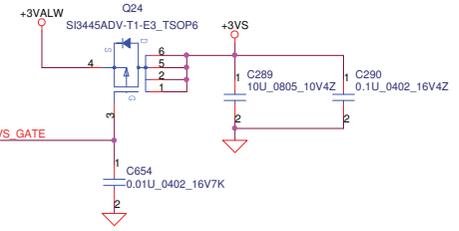
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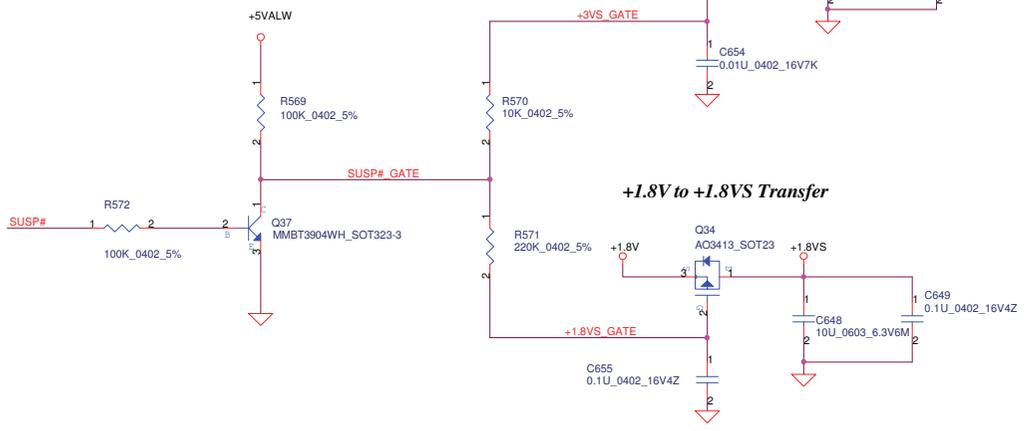
+5VALW to +5VS Transfer



+3VALW to +3VS Transfer

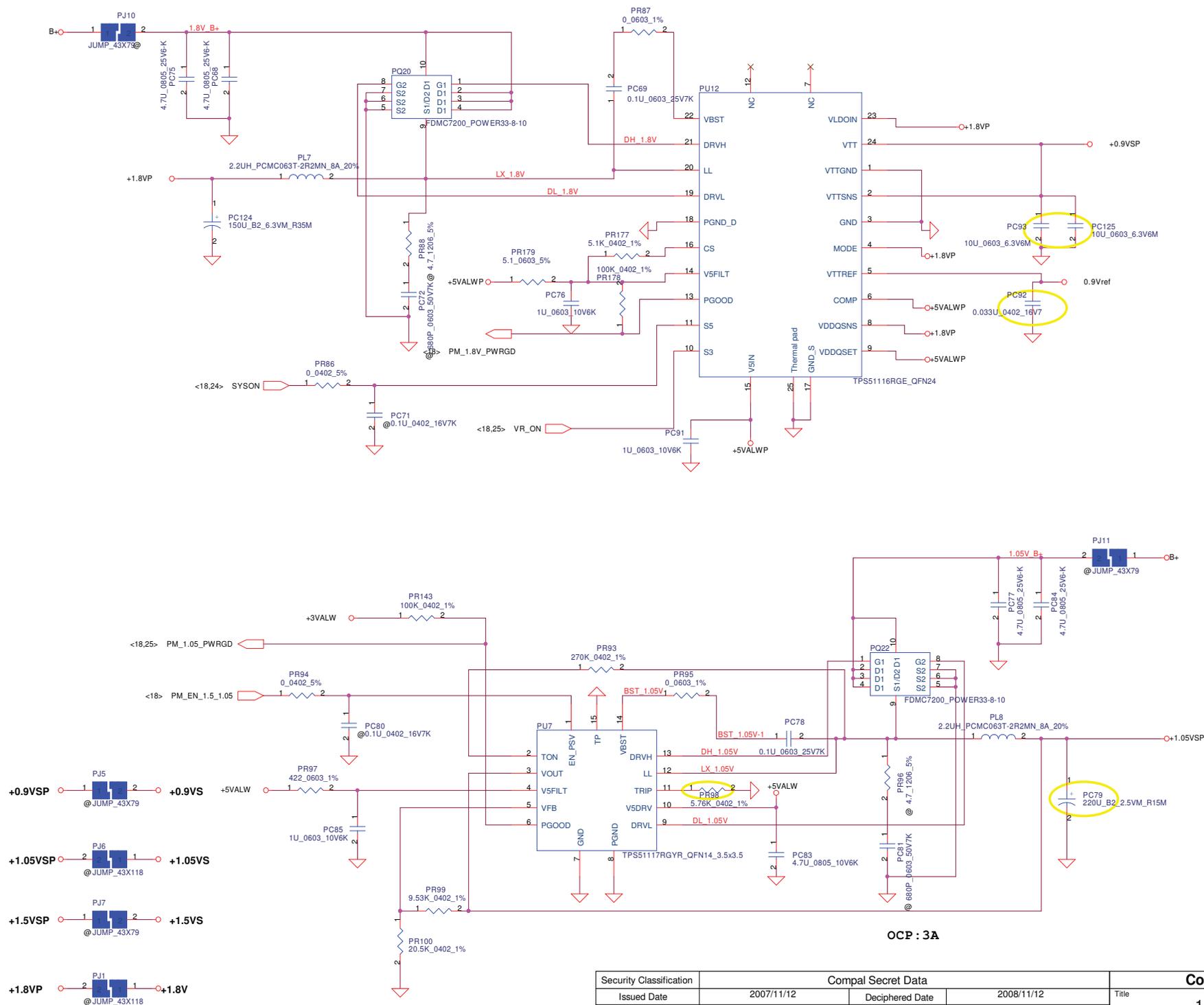


+1.8V to +1.8VS Transfer



PRODUCT SUMMARY		
V _{gs}	r _{DS(on)}	ID (A)
-8	0.042 @ V _{GS} = .4.5 V	+5.6
	0.060 @ V _{GS} = .2.5 V	+4.7
	0.080 @ V _{GS} = .1.8 V	+2.9

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Date: Monday, April 06, 2009			Sheet	22 of 28

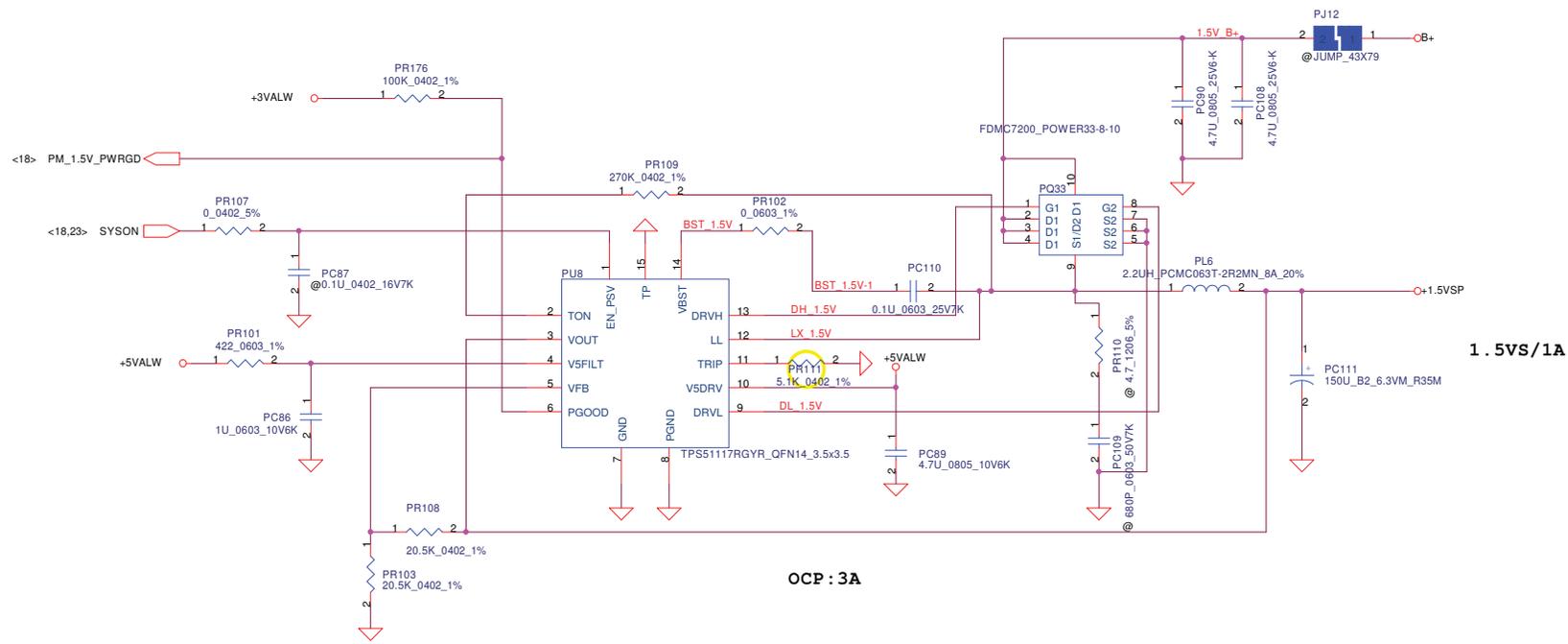


1.05V/1.5A

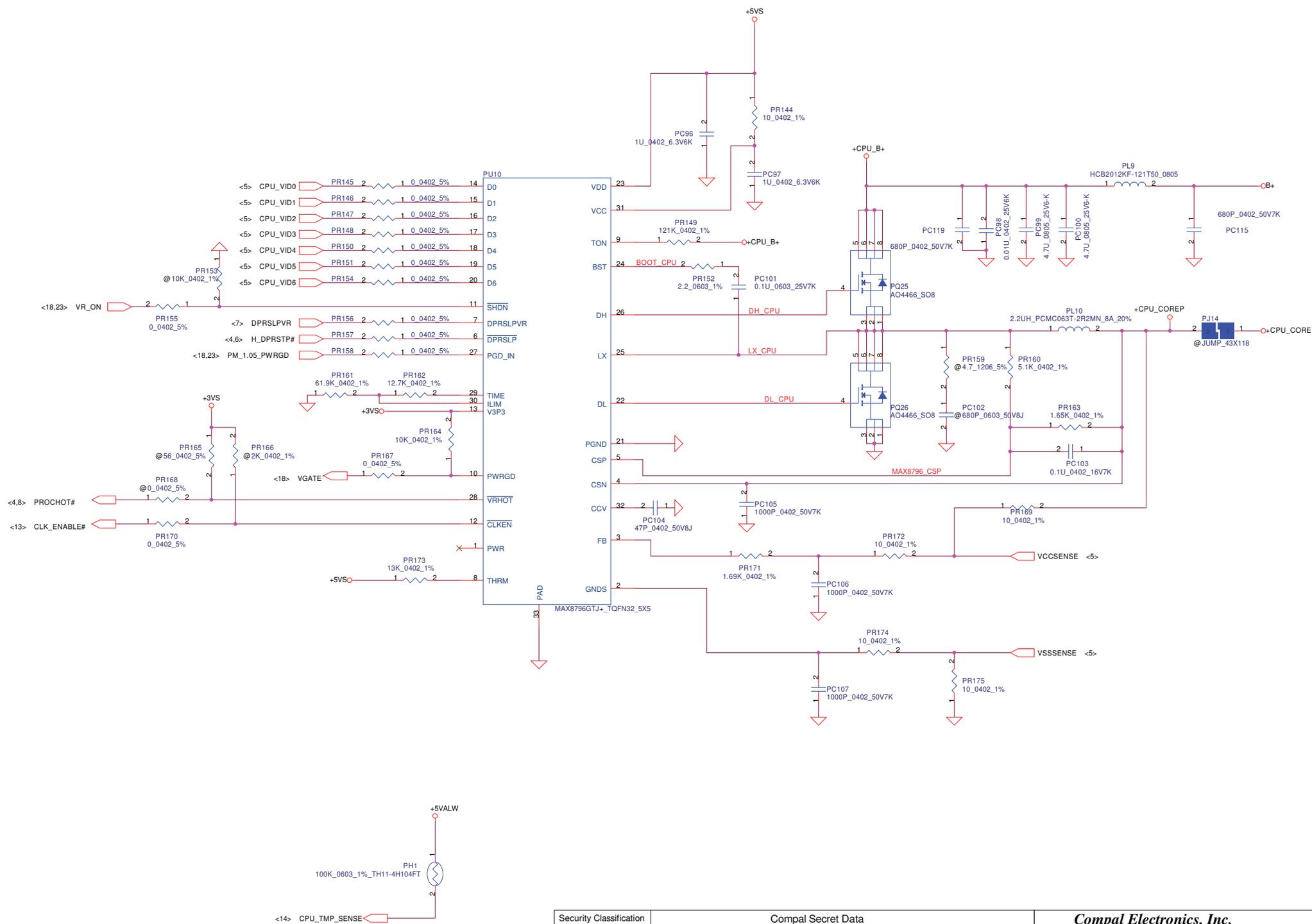
OCP : 3A

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Title		
1.05V / 1.8V		
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ITEM	Fixed Issue/Reason for change	REV	PAGE	CHANGE LIST	
1	1.DC/DC switch Capacity voltage tolerance 2.B+ is over Q23, Q24, Q34 Gate Voltage and cause the leakage on +1.8VS & +3VS.	0.2	PAGE 22	1.Change C653, C654, C655 to 50V tolerance 2.Q23, Q24, Q34 Gate pull high voltage change to +5VALW	090310
2	1.Change JP19 to I-PEX 30Pin.	0.2	PAGE 20	1.Change JP19 to I-PEX 30Pin(I-PEX_20439-R30E-02).	090310
3	1.Del reserve DB port Conn. of EC	0.2	PAGE 21	1.Delete JP26. 2.Delete LPC DB port.	090310
4	1.Follow the LCD SPEC to add Pin 20 LVDS_EN	0.2	PAGE 15 PAGE 18	1.Add LVDS_EN on JP3 Pin20 and U35 B10 2.Add R200 100K pull high.	090310
5	1.BT is USB 2.0 Full Speed. Change USB port to Port 4	0.2	PAGE 14 PAGE 20	1.BT USB change to USB20 port 4. 2.3G USB change to USB20 port 7.	090310
6	1.C5, C8, C550 change to lower cost and lower ESR.	0.2	PAGE 05	1.C5, C8, C550 change from 220U_B15G_70m ohm to 220U_B2_15m ohm	090311
7	1.C52, C55, C63, C153 change to lower cost and lower ESR.	0.2	PAGE 09	1.C52, C55, C63, C153 change from 150U_B15G_70m ohm to 150U_B2_35m ohm	090311
8	1.Add HP, HP/MIC Auto Detect circuit.	0.2	PAGE 16	1.Add U38 Comparator circuit.	090312
9	1.USB port change to on board. 1.SATA FPC connector change to TOP conduction.	0.2	PAGE 15	1.Change JP1 and add JP2 TYCO_2009104. 2.Change JP20 from 87152 type to 87151type.	090320
10	1.Panel Backlight voltage change for efficient.	0.2	PAGE 15	1.Add B+ for LED backlight supply.	090326
11	1.GL831 +1.8V pin input output separate for decrease the power consumption	0.2	PAGE 19	1.U37 Pin8, 24, 35(CVDD1, 2, 3) use internal 3.3V to 1.8V regulator 2.Other 1.8V pins use system +1.8VS power.	090326
12	1.G-Sensor application need always power.	0.2	PAGE 20	1.Change Interface to EC SMBUS1, change to +3VALW power 2.Add level shift for SMBUS I/F (+5VALW to +3VALW.) 3.Change G-Sensor from MMA7660 to MMA7455	090326
13					090327
14					

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					LA-3481P	0.3
				Date:	Monday, April 06, 2009	Sheet 28 of 28